

FORM PTO-1449
REV. 7.801U.S. Department of Commerce
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INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

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U.S. PATENT DOCUMENTS

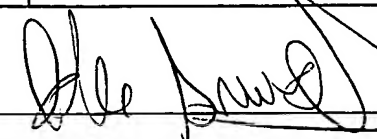
Examiner Initial	Document Number	Date	Name	Class	Sub-Class	Filing Date if Appropriate

FOREIGN PATENT DOCUMENTS

FOREIGN-AREA DOCUMENTS											
	Document Number					Date	Country	Class	Sub-Class	Translation	
										Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Wei and Antoniadis, "Minimizing Floating-Body-Induced Threshold Voltage Variation in Partially Depleted SOI CMOS," <i>IEEE Electron Device Letters</i> , Vol. 17, No. 8 (August 1996) at pp. 391-394.

Examiner 	Date Considered 9/27/02
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* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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